

Class	Subclass
ISSUE CLASSIFICATION	

PATENT NUMBER

## U.S. UTILITY Patent Application

**O.I.P.E.**

PATENT DATE

**SCANNED**

SCANNED 20.03 Q.A. Am

APPLICATION NO. 09/822643	CONT/PRIOR	CLASS 711	SUBCLASS 141	ART UNIT <del>2105</del> 2188	EXAMINER <del>M. K.</del> PORTKA
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## APPLICANTS

## Blaise Cendrars

**TITLE**

## Controlling cache memory in external chipset using processor

PTO-2040  
12/89

# ISSUING CLASSIFICATION

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)  _____ (Primary Examiner)  _____ (Legal Instruments Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____			<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.			<b>ISSUE BATCH NUMBER</b>	
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